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Godin, J.; Riet, M.; Blayac, S.; Berdaguer, P.; Dhalluin, V.; Alexandre, F.; Kahn, M.; Piquier, A.; Kasbari, A.; Moulou, J.; Konczykowska, A.; Gallium Arsenide Integrated Circuit (GaAs IC) Symposium, 2002. 24th Annual Technical Digest

20-23 Oct. 2002 Page(s):215 - 218

IEEE CNF

2. Toward a Multiple Clock/Voltage Island Design Style for Power-Aware Processors

Talpes, E.; Marculescu, D.; Very Large Scale Integration (VLSI) Systems, IEEE Transactions on Volume 13, Issue 5, May 2005 Page(s):591 - 603

IEEE JNL

3. Energy and performance models for clocked and asynchronous communication

Stevens, K.S.; Asynchronous Circuits and Systems, 2003. Proceedings. Ninth International Symposium on 12-15 May 2003 Page(s):56 - 66

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